

Fysetc Catalyst Bullant

Catalyst	Bullant
Repo https://github.com/FYSETC/Catalyst_Kit_V2.0	https://github.com/FYSETC/FYSETC_Bullant_V1.0
Image https://github.com/FYSETC/Catalyst_Kit_V2.0/blob/449f84ea78f707c273e316bcebbfd13e400e57/software/update_voron0_2_20240108.rar https://github.com/FYSETC/FYSETC-Voron-0.2-Pro/tree/main/0.2%20R1%20V1.1	https://github.com/FYSETC/FYSETC_Bullant_V1.0/blob/90d785366823cc0e31b075f60f8dc90145677ea5/software/Bullant_Ambian_OS_Klipper_Build_in_V1.0.rar
Hostname maixsense	maixsense
User / Password Default: linaro/linaro, root/root	Default: bullant/armbian, root/armbian
Network Kein Autonegotiation auf 100mBit	100mBit
Schaltplan https://github.com/FYSETC/Catalyst_Kit_V2.0/blob/449f84ea78f707c273e316bcebbfd13e400e57/hardware/CATALYST%20V2.0%20SCH.pdf	https://github.com/FYSETC/FYSETC_Bullant_V1.0/blob/90d785366823cc0e31b075f60f8dc90145677ea5/Bullant%20V1.0%20SCH.pdf
Pinout https://github.com/FYSETC/Catalyst_Kit_V2.0/blob/449f84ea78f707c273e316bcebbfd13e400e57/hardware/CATALYST%20V2.0%20Pinout.png https://github.com/FYSETC/Catalyst_Kit_V2.0/blob/449f84ea78f707c273e316bcebbfd13e400e57/hardware/CATALYST%20V2.0%20%4%B0%8D%ES%8F%4B%E5%9B%8E.pdf	https://github.com/FYSETC/FYSETC_Bullant_V1.0/blob/90d785366823cc0e31b075f60f8dc90145677ea5/Bullant%20V1.0%20PINOUT.pdf
CPU / RAM CM68 core based on RK3568 Quad Core 2GB Ram	Allwinner R329 Dual-Core Cortex-A53 ARM CPU; 512 256MB DRAM
µController STM32F446	RP2040
USB 2x USB 3.0	1x USB 0
Treiber TMC2209	TMC2209 + GC6609
RGB vorhanden	vorhanden
Fans 3x geregelt bis 24V	1 Fan
Temp 3x Temp	1x Temp
CAN Bus vorhanden	-
Sonstiges 2x SPI, extra 2x UART	-

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Last update: **2024/05/14 16:48**

