

Fysetc Catalyst Bullant

Übersicht

Catalyst	Bullant
Repo https://github.com/FYSETC/Catalyst_Kit_V2.0	https://github.com/FYSETC/FYSETC_Bullant_V1.0
Image https://github.com/FYSETC/Catalyst_Kit_V2.0/blob/449f84ea78f707c273e316bcebbfd13e400e57/software/update_voron0_2_20240108.rar https://github.com/FYSETC/FYSETC-Voron-0.2-Pro/tree/main/0.2%20R1%20V1.1	https://github.com/FYSETC/FYSETC_Bullant_V1.0/blob/90d78536b823cc0e31b075f60f8d90145677ea5/software/Bullant_Armbian_OS_Klipper_Build_in_V1.0.rar
Hostname maixsense	maixsense
User / Password Default: linaro/linaro, root/root	Default: bullant/armbian, root/armbian
Network Kein Autonegotiation auf 100Mbit	100mBit
Schaltplan https://github.com/FYSETC/Catalyst_Kit_V2.0/blob/449f84ea78f707c273e316bcebbfd13e400e57/hardware/CATALYST%20V2.0%20SCH.pdf	https://github.com/FYSETC/FYSETC_Bullant_V1.0/blob/90d78536b823cc0e31b075f60f8d90145677ea5/Bullant%20V1.0%20SCH.pdf
Pinout https://github.com/FYSETC/Catalyst_Kit_V2.0/blob/449f84ea78f707c273e316bcebbfd13e400e57/hardware/CATALYST%20V2.0%20PinOut.png https://github.com/FYSETC/Catalyst_Kit_V2.0/blob/449f84ea78f707c273e316bcebbfd13e400e57/hardware/CATALYST%20V2.0%20E4%BD%8D%E5%8F%87%E5%9B%BE.pdf	https://github.com/FYSETC/FYSETC_Bullant_V1.0/blob/90d78536b823cc0e31b075f60f8d90145677ea5/Bullant%20V1.0%20PINOUT.pdf
CPU / RAM C1M68 core based on RK3568 Quad Core 2GB Ram	Allwinner R329 Dual-Core Cortex-A53 ARM CPU; 512MB DRAM
µController STM32F446	RP2340
USB 2x USB3.0	1x USB2.0
Treiber TMC2209	TMC2209 + GC6609
RGB vorhanden	vorhanden
Fans 3x geregelt bis 24V	1 Fan
Temp 3x Temp	3x Temp
CAN Bus vorhanden	
Sonstiges I2C, SPI, extra 2x UART	

Besonderheiten

- **M36 Boards nicht Pinkompatibel** (Anschluss)
- keine root Partition mit armbian Konfig
- Image für Catalyst laut Pi Imager defekt weil Bytesumme nicht durch 512 teilbar
- Catalyst Board geht bei 100Mbit Netzwerk nicht Online!!
- Zugriff auf Bullant per seriellem Port (für Init) → unpraktisch

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